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For Immediate Release

Cypress Samples World's First Programmable SONET/SDH OC-48 PHY

*SONET/SDH Chip Combines Programmable Logic, High-Speed Serial Interface
and Abundant Communications Memory, Targeting Optical Terminators,
SONET/SDH Routers and Add-Drop MUX Subsystems*

SAN JOSE, Calif., May 4, 2001 – Cypress Semiconductor (NYSE: CY) today announced the availability of samples of the PSI2G100S, the second in its family of Programmable Serial Interface (PSI™) chips. The PSI2G100S integrates a SONET/SDH OC-48 (2.5 Gbps) transceiver, clock data recovery (CDR) circuitry, a SERDES, 100k gates of programmable logic, and 240 Kbits of communications memory, targeting OC-48/STM-12 optical terminators, SONET/SDH routers and add-drop MUX subsystems.

The PSI2G100S closely follows the introduction of the PSI2G100, the world's first 2.5 Gbps programmable PHY. PSI devices offer operating speeds from 1 x 2.5 Gbps to 8 x 1.5 Gbps to support high-bandwidth applications.

PSI devices combine the flexibility, predictable timing, and ease-of-use of Cypress CPLDs with a SERDES, communications memory and phase-locked loops (PLLs). Cypress's *Warp*™ R6.1 software enables a seamless programming interface to allow design engineers to easily integrate custom IP with the SERDES via HDL blocks, HDL text, or graphical state machines. Cypress is the only company to offer a SONET/SDH OC-48 compliant, 2.5 Gbps SERDES, programmable logic gates, design entry, synthesis and verification in an integrated, single-chip solution.

"Cypress is delivering the world's first programmable SONET/SDH OC-48 PHY," said Geoff Charubin, director of marketing for Cypress's data communications division. "The PSI2G100S brings the speed and flexibility of our programmable OC-48 SERDES to SONET/SDH applications, enabling communications solutions designers to develop cutting edge optical solutions and get them to market quickly—and into the OC-48 segment, the sweet spot of the WAN market."

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The PSI2G100S is perfectly suited for both port and backplane solutions in a typical line card application. Its programmability enables customers to create customized and flexible solutions for the parallel-side interface—for example, taking an OC-48 data stream from the serial side and converting it to a 32- or 64-bit parallel-bus topology on the other.

The devices in the PSI family provide a programmable interface to a SERDES that is compatible with various physical layer transmission media – fiber optic modules, copper cables and circuit board traces. Along with optimized communications memory (such as dual-ported and FIFO memories), logic and PLLs, the ICs will provide parallel programmable I/Os supporting LVCMOS, LVTTL, 3.3 Volt PCI, SSTL2, SSTL3, HSTL, and GTL+ inputs. The combined serial bandwidth of 200 Mbps to 12 Gbps will allow PSI devices to meet the requirements of a broad range of market segments.

Pricing and Availability

The PSI2G100S is the second PSI family product. Sampling now, the PSI2G100S is available in a 456-ball BGA package. Volume pricing for the SONET/SDH OC-48, 2.5 Gbps, 100k-gate, devices is \$150. For more information please go to <http://www.cypress.com/psi>.

Photo

For a high-resolution digital photo of the PSI2G100S, please go to <http://www.cypress.com/press/photo/psi2g100s.html>.

About Cypress

Cypress Semiconductor is “Driving the Communications Revolution”™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-performance communication devices, high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated

solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,700 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide Web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810).

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PSI, *Warp* and “Driving the Communications Revolution” are trademarks of Cypress Semiconductor.